

FIG. 1

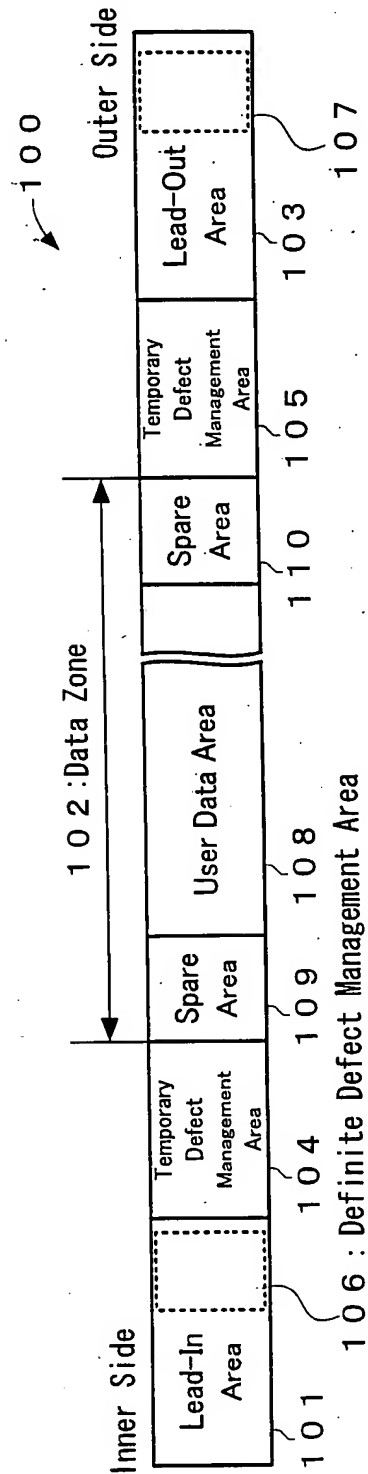


FIG. 2

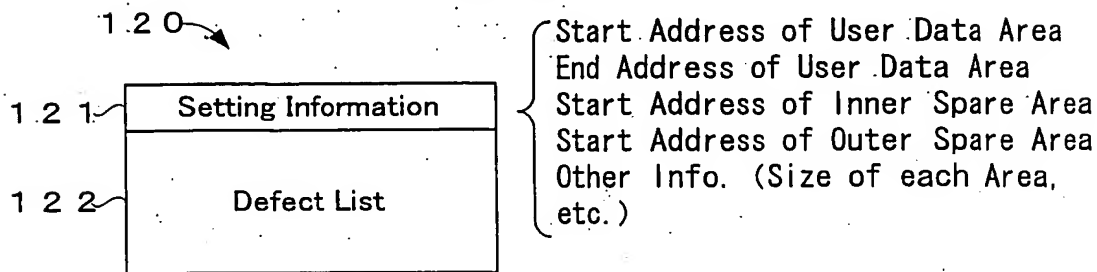


FIG. 3

Table illustrating the data structure layout, with pointer 122 indicating the start of the data area.

Defect Address	Spare Address	Other Info.
Address aaaa	Address gggg	
Address bbbb	Address kkkk	
Address cccc	Address mmmm	
Address dddd	Address nnnn	
⋮	⋮	

FIG. 4

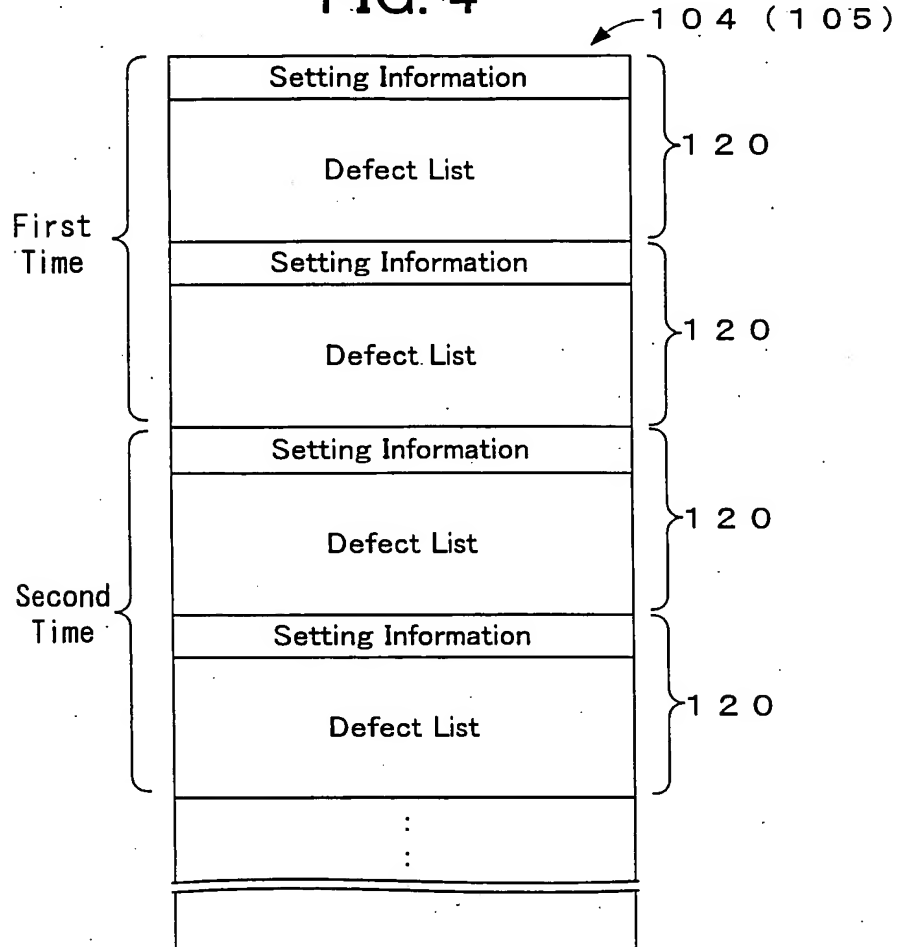


FIG. 5

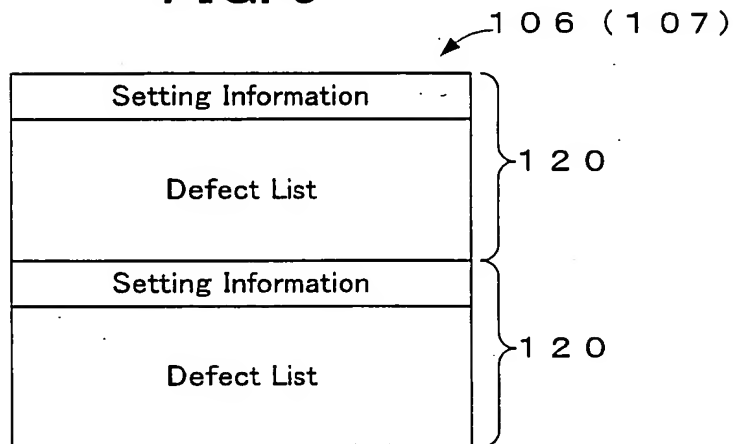


FIG. 6

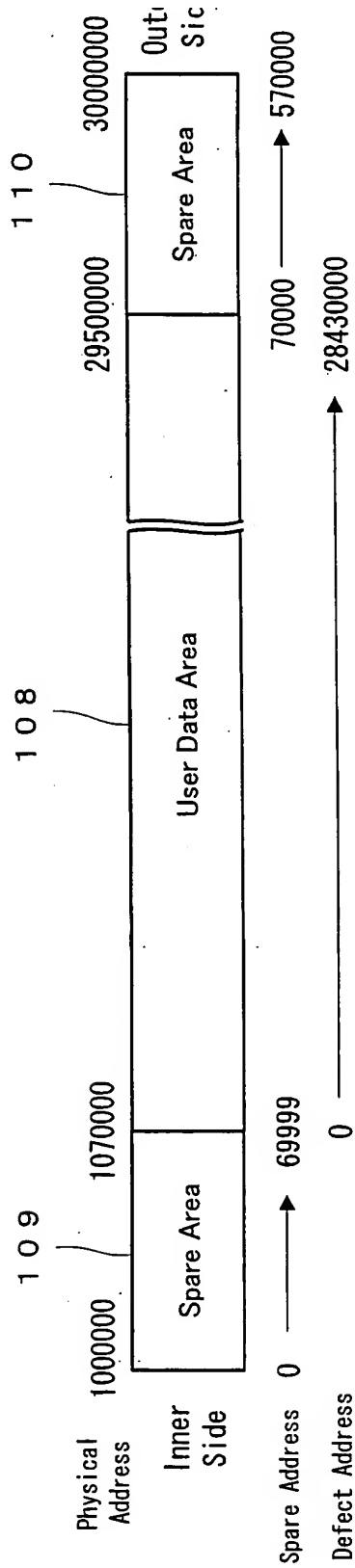


FIG. 7(a)

	4 Bytes	3 Bytes
	Defect Address	Spare Address
DFL Entry 1	Address A1	Address a1
DFL Entry 2	Address B1	Address b1
DFL Entry 3	Address C1	Address c1
DFL Entry 4	Address D1	Address d1

Defect List Generated by using
Offset Address

FIG. 7(b)

	4 Bytes	4 Bytes
	Defect Address	Spare Address
DFL Entry 1	Address A2	Address a2
DFL Entry 2	Address B2	Address b2
DFL Entry 3	Address C2	Address c2
DFL Entry 4	Address D2	Address d2

Defect List Generated by using
Physical Address

FIG. 8

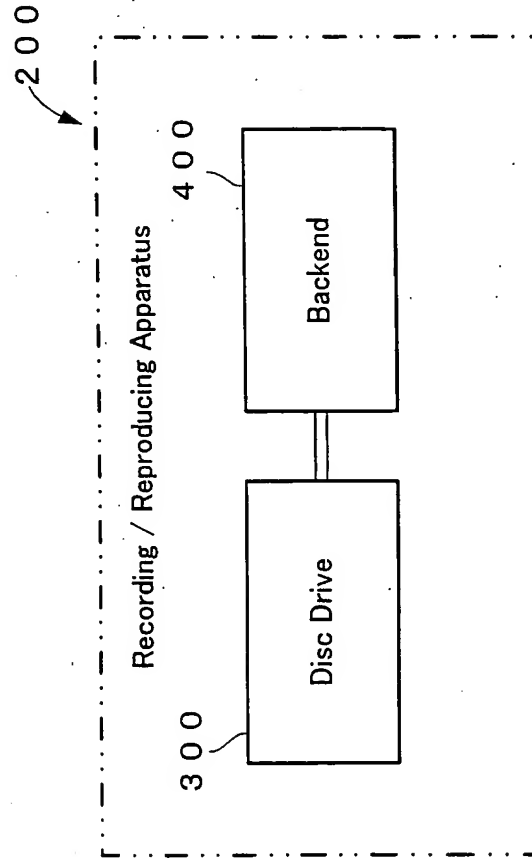


FIG. 9

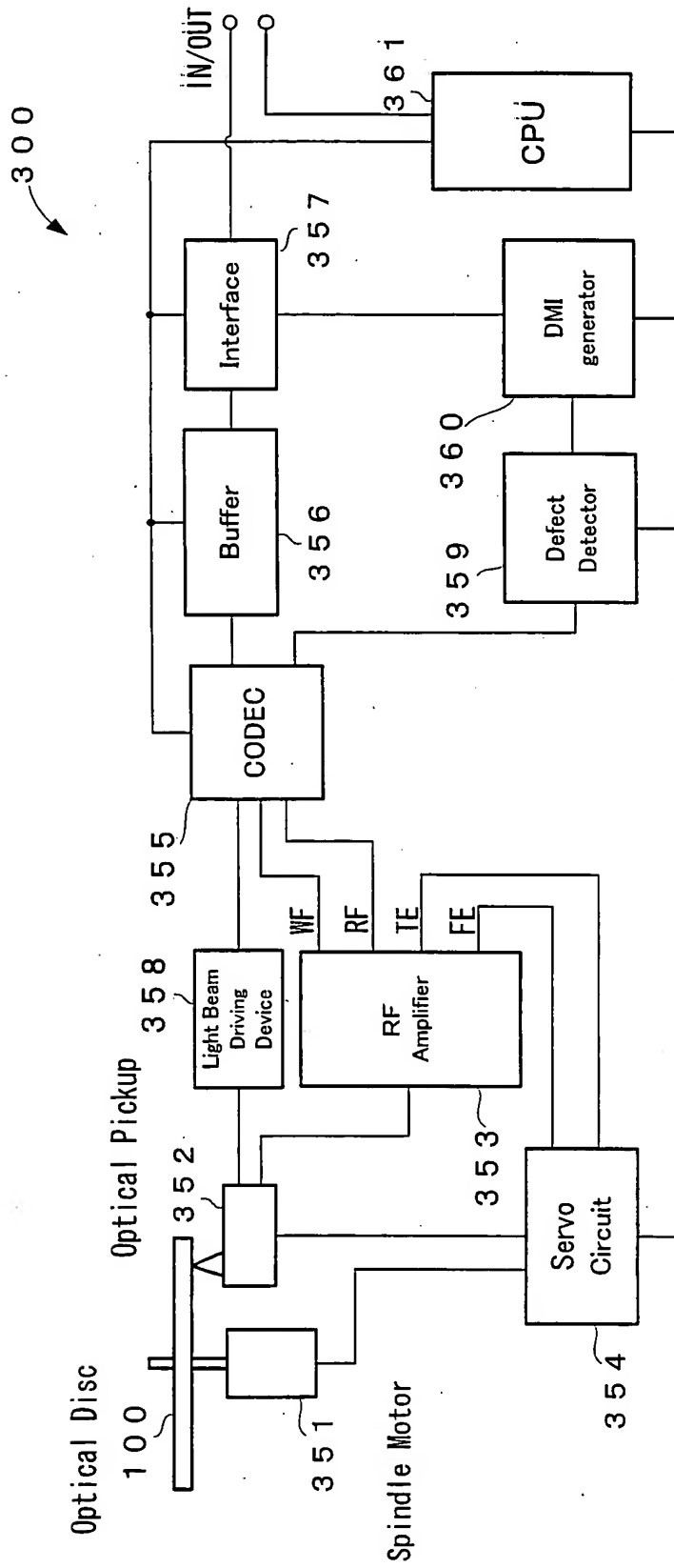
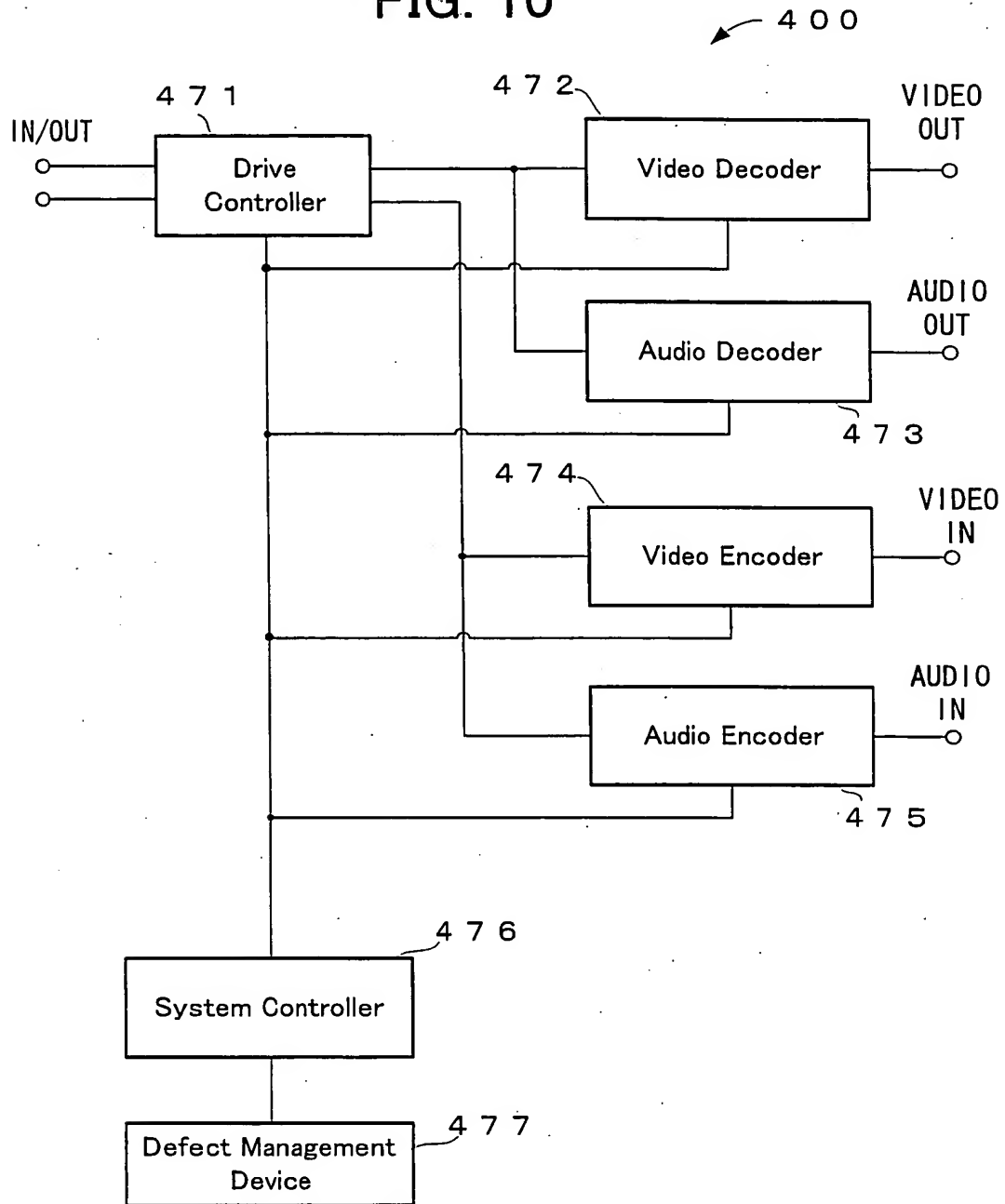


FIG. 10



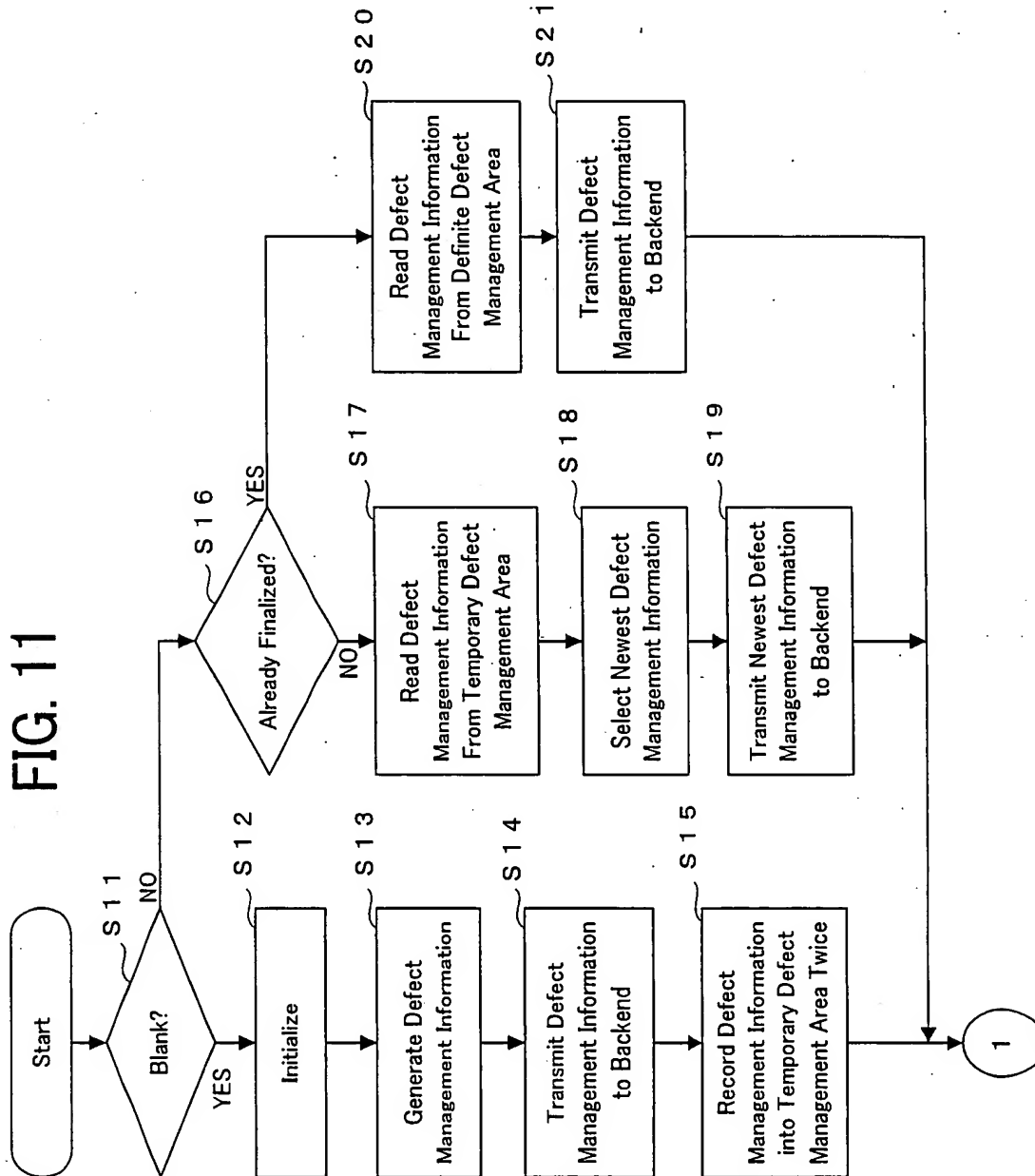


FIG. 12

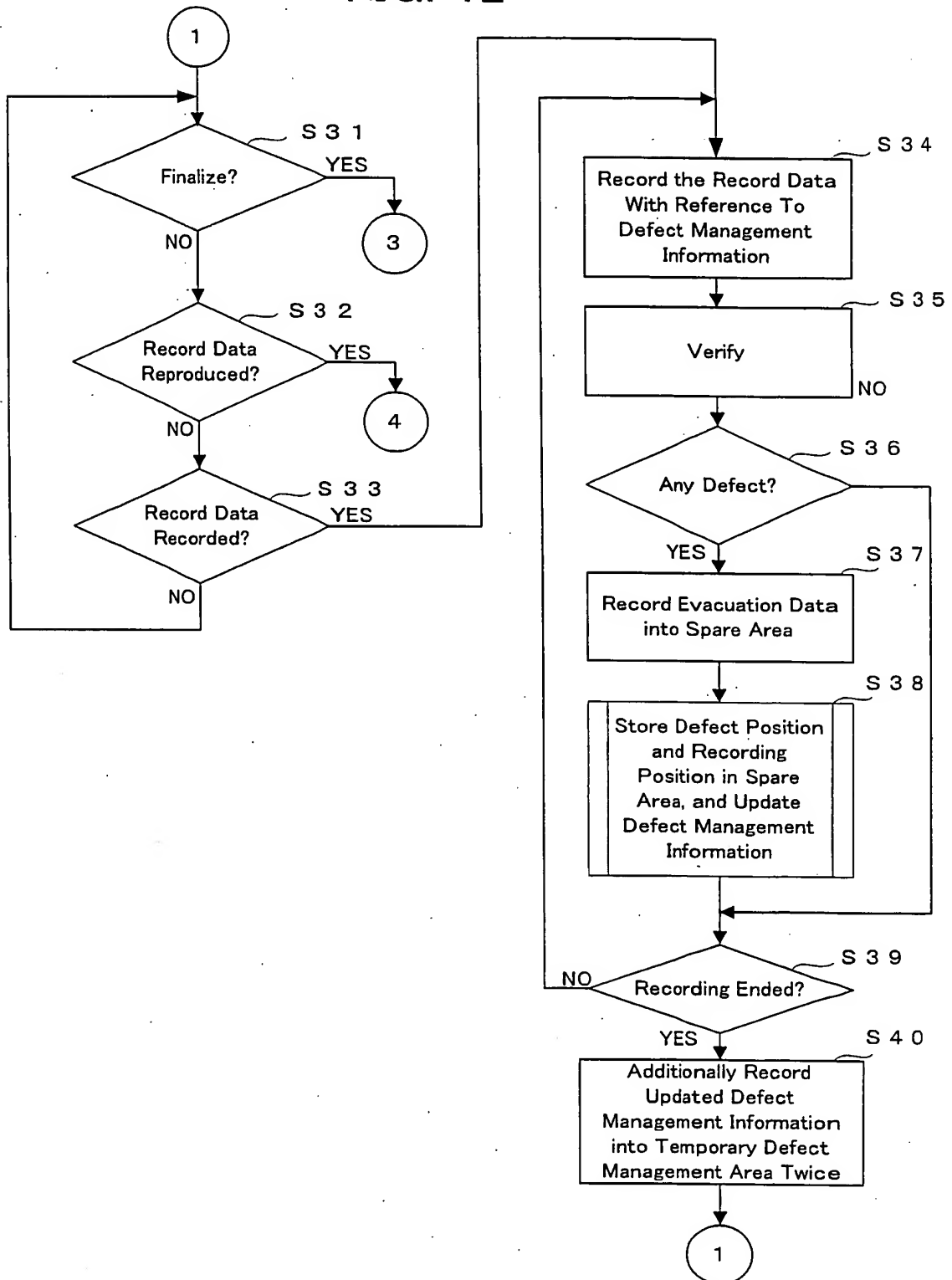


FIG. 13

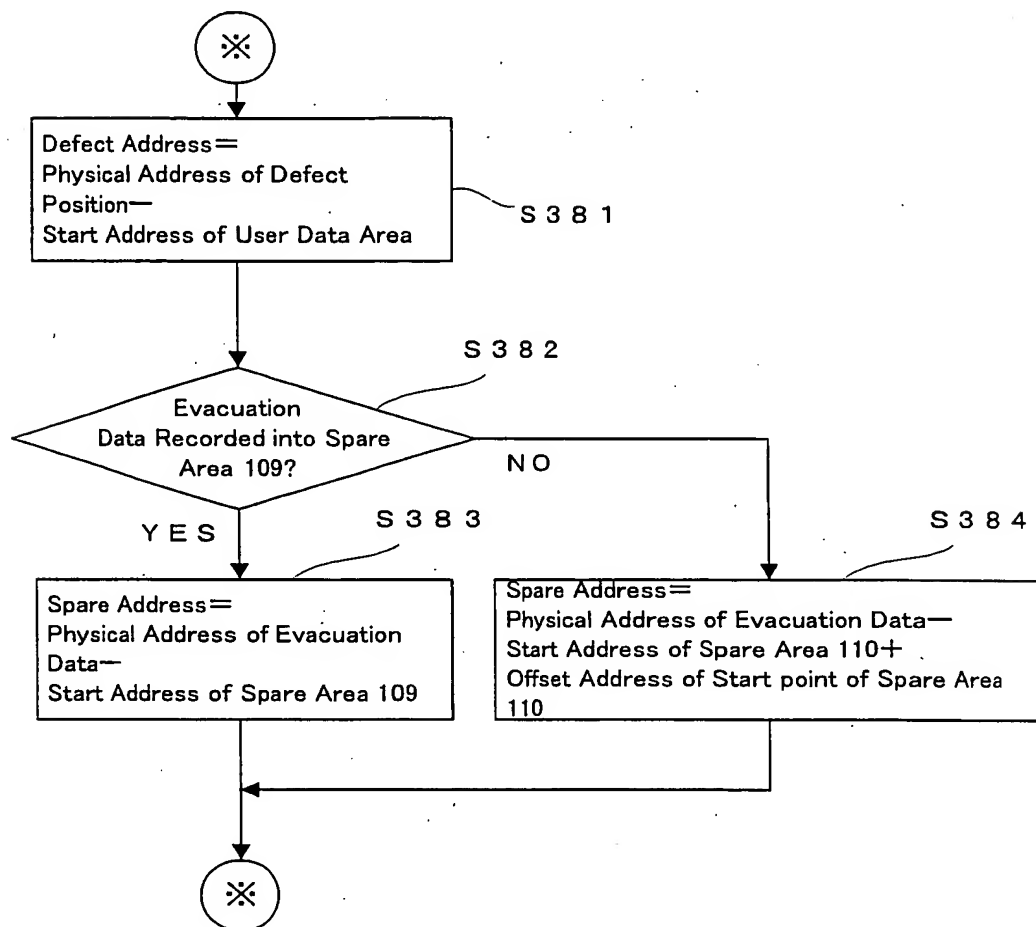


FIG. 14

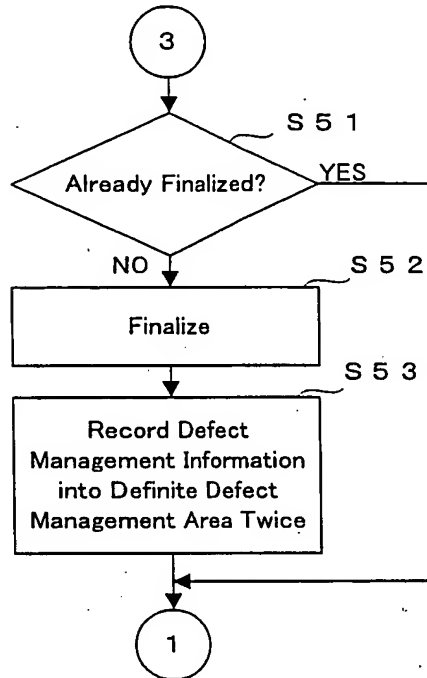


FIG. 15

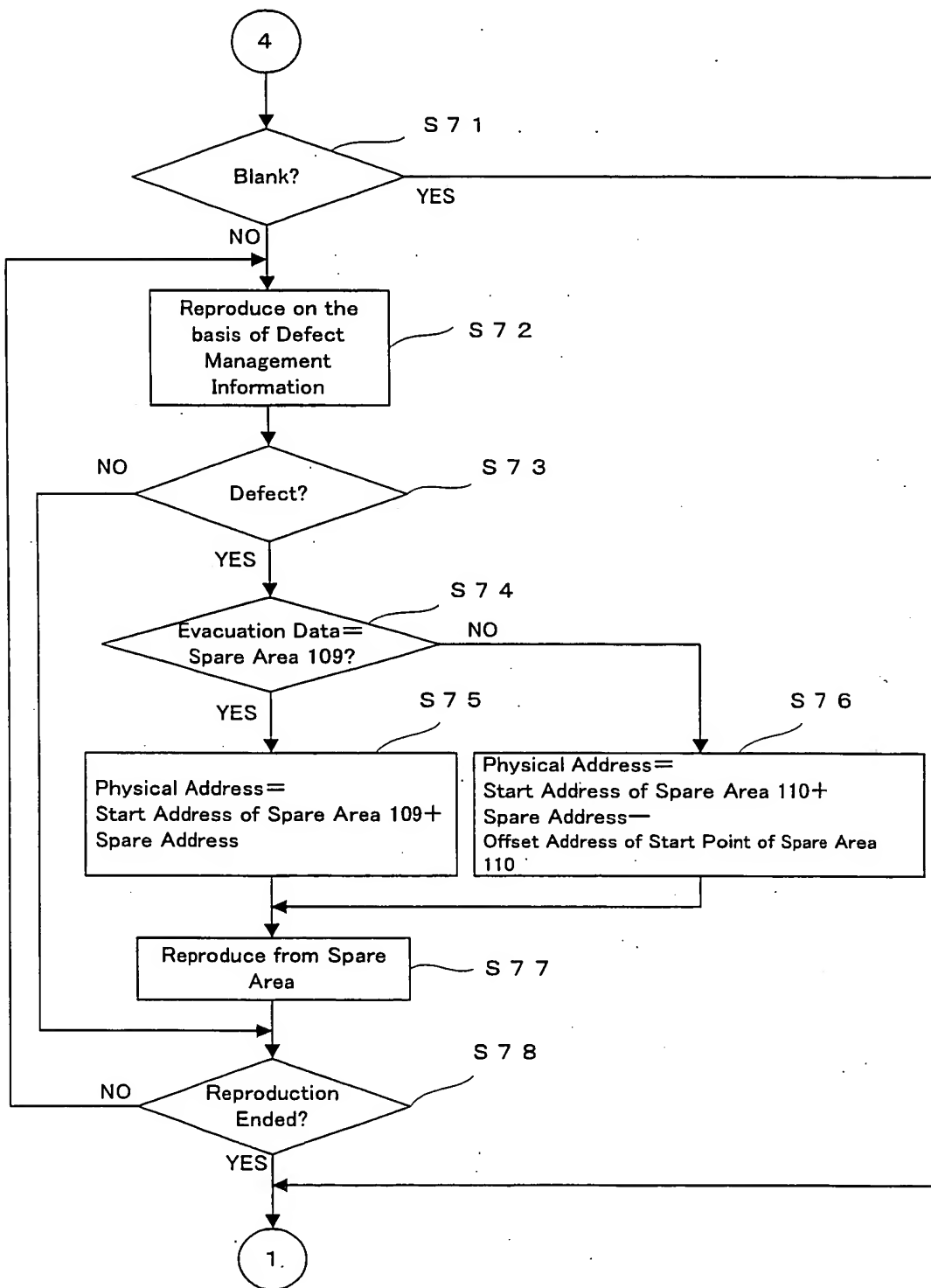


FIG. 16

